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June 10, 2005

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 6,876,593 *B2*
Issued: April 5, 2005
For: **METHOD AND APPARATUS FOR PARTIAL
REFRESJOMG OF DRAMS**
Inventor: Shi et al.
Our File No. 42390.P16962

**Certificate
JUL 14 2005
of Correction**

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Edwin H. Taylor
Reg. No. 25,129

EHT/kh
Enclosures

JUL 15 2005

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,876,593 *B2*

DATED : April 5, 2005

INVENTOR(S) : Shi et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 2, at line 67, delete "10" and insert --110--.

In column 11, at line 7, insert --During normal operation of computer system 700, CPU 710 executes instructions causing CPU 710 to write marking data into marking buffer 745 to mark a row within a memory array, such as memory array 751a of memory device 750a, as not having data to be preserved. As a result, when memory controller 740 is to command that a row within memory array 751a be refreshed, memory controller 740 will obtain marking data from marking buffer 745 to identify a row marked as having data to be preserved, and the identified row will be a row other than the one that CPU 710 was just caused to mark as not having data to be preserved. Memory controller 740 will then transmit a row address identifying that other row with the refresh command to memory device 750a, causing memory device 750a to carry out a refresh operation on that specific row within memory array 751a.--.

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PATENT NO. 6,876,593

Certificate of Correction (PTO Form 1050)-Amended

JUL 15 2005